

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Kazuhisa Saito, et al.

Examiner: Felix O. Figueroa

Filing Date: November 26, 2003

Art Unit: 2833

Title: Electronic Component and  
Method of Manufacturing the  
Same

November 26, 2003  
San Francisco, California

Commissioner of Patents  
P.O. Box 1450  
Alexandra, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Pursuant to the duty of disclosure set forth in 37 CFR § 1.56, Applicant(s) respectfully submit the following information disclosure statement.

The disclosed references are:

US 5,957,736; Moriuchi, et al., issued September 28, 1999, filed December 8, 1997.

US 5,453,017; Belopolsky, issued September 26, 1995, filed December 9, 1994.

US 4,087,906; Cobaugh, et al., issued May 9, 1978, filed July 29, 1975.

US 6,049,039; Fushimi, issued April 11, 2000, filed November 24, 1997.

US 3,989,331; Hanlon, issued November 2, 1976, filed August 21, 1974.

JP 08 213070; Japanese Patent Office; published August 20, 1996, Patent Abstracts of Japan

A copy of the European Search Report is enclosed herewith.

The present application is a divisional application of U.S. patent application 09/844,226 filed 04/27/2001 (the "Parent Application"). References cited during prosecution of the Parent Application, either disclosed by Applicants or reported by the Examiner, are listed on the

accompanying form PTO-1449. Inasmuch as these references should be available from the files of the parent application, Applicants have not submitted copies with this statement.

Applicants respectfully request that the Examiner initial the cited references shown on the enclosed form PTO-1449 and that the references be made of record in the present application.

Respectfully submitted,

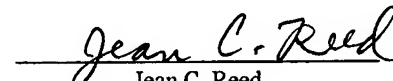


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I certify that this Information Disclosure Statement and all enclosed materials are being deposited with the United States Postal Service on November 26, 2003 as "Express Mail," mailing label EV326498555US, in an envelope addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Jean C. Reed

